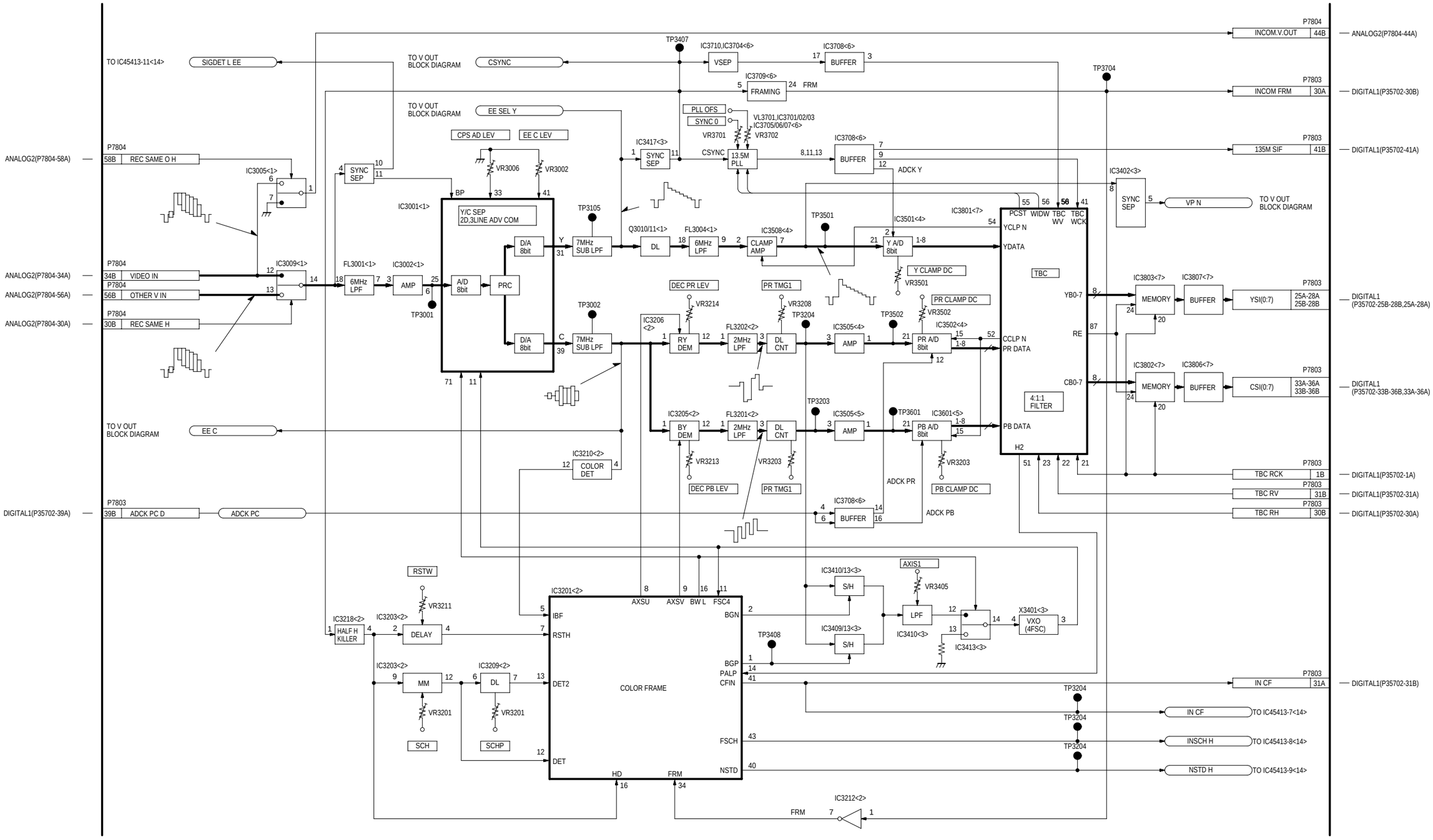
TO IC45413-11<14>
SIGDET L EE
TO V OUT BLOCK DIAGRAM
CSYNC
EE SEL Y
EE C
DIGITAL1(P35702-39A)
ADCK PC D
ADCK PC
ANALOG2(P7804-58A)
REC SAME O H
ANALOG2(P7804-34A)
VIDEO IN
ANALOG2(P7804-56A)
OTHER V IN
ANALOG2(P7804-30A)
REC SAME H
IC3005<1>
IC3009<1>
IC3001<1>
Y/C SEP 2D,3LINE ADV COM
IC3002<1>
FL3001<1>
6MHz LPF
AMP
A/D 8bit
PRC
D/A 8bit
7MHz SUB LPF
TP3001
TP3002
TP3105
SYNC SEP
CPS AD LEV
EE C LEV
VR3006
VR3002
BP
Q3010/11<1>
DL
FL3004<1>
6MHz LPF
IC3508<4>
CLAMP AMP
TP3501
IC3501<4>
Y A/D 8bit
Y CLAMP DC
VR3501
IC3801<7>
PCST WIDW TBC WV TBC WCK
YCLP N
YDATA
TBC
IC3417<3>
SYNC SEP
TP3407
IC3710,IC3704<6>
VSEP
IC3708<6>
BUFFER
IC3709<6>
FRAMING
FRM
PLL OFS
SYNC 0
VL3701,IC3701/02/03
IC3705/06/07<6>
VR3701
VR3702
13.5M PLL
8,11,13
ADCK Y
IC3402<3>
VP N
TO V OUT BLOCK DIAGRAM
TP3704
P7804
INCOM.V.OUT 44B
ANALOG2(P7804-44A)
P7803
INCOM FRM 30A
DIGITAL1(P35702-30B)
135M SIF 41B
DIGITAL1(P35702-41A)
IC3803<7>
IC3807<7>
MEMORY
BUFFER
YSI(0:7) 25A-28A 25B-28B
DIGITAL1 (P35702-25B-28B,25A-28A)
YB0-7
RE
IC3802<7>
IC3806<7>
CSI(0:7) 33A-36A 33B-36B
DIGITAL1 (P35702-33B-36B,33A-36A)
CB0-7
4:1:1 FILTER
DEC PR LEV
VR3214
PR TMG1
VR3208
PR CLAMP DC
VR3502
TP3204
TP3502
IC3502<4>
PR A/D 8bit
CCLP N
PR DATA
IC3206 <2>
RY DEM
FL3202<2>
2MHz LPF
DL CNT
IC3505<4>
AMP
IC3205<2>
BY DEM
FL3201<2>
TP3203
IC3505<5>
TP3601
IC3601<5>
PB A/D 8bit
PB DATA
H2
VR3213
DEC PB LEV
VR3203
PR TMG1
ADCK PR
PB CLAMP DC
IC3210<2>
COLOR DET
IC3708<6>
BUFFER
ADCK PB
TBC RCK 1B
DIGITAL1(P35702-1A)
TBC RV 31B
DIGITAL1(P35702-31A)
TBC RH 30B
DIGITAL1(P35702-30A)
RSTW
VR3211
IC3218<2>
HALF H KILLER
IC3203<2>
DELAY
IC3201<2>
COLOR FRAME
IBF
RSTH
DET2
DET
HD
FRM
AXSU
AXSV
BW L
FSC4
BGN
BGP
PALP
CFIN
FSCH
NSTD
IC3203<2>
MM
IC3209<2>
DL
VR3201
SCH
SCHP
IC3410/13<3>
S/H
IC3409/13<3>
AXIS1
VR3405
LPF
IC3410<3>
IC3413<3>
X3401<3>
VXO (4FSC)
TP3408
IC3212<2>
IN CF
TO IC45413-7<14>
INSCH H
TO IC45413-8<14>
NSTD H
TO IC45413-9<14>
P7803
IN CF 31A
DIGITAL1(P35702-31B)